

FIG. 1

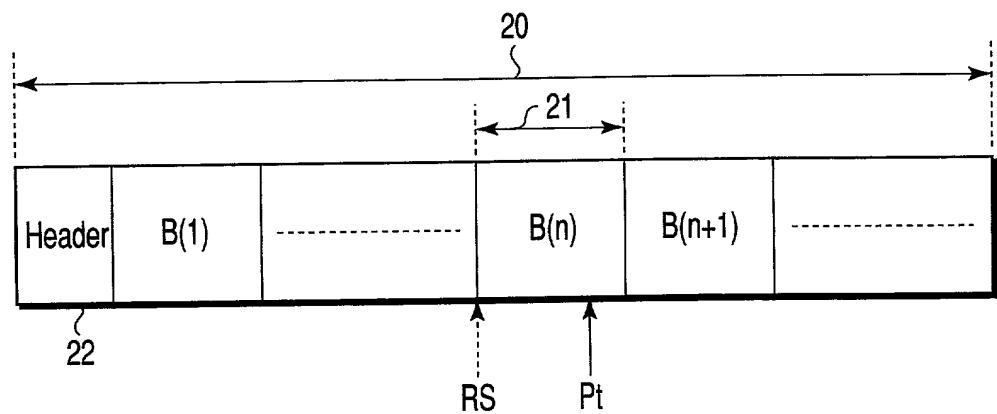
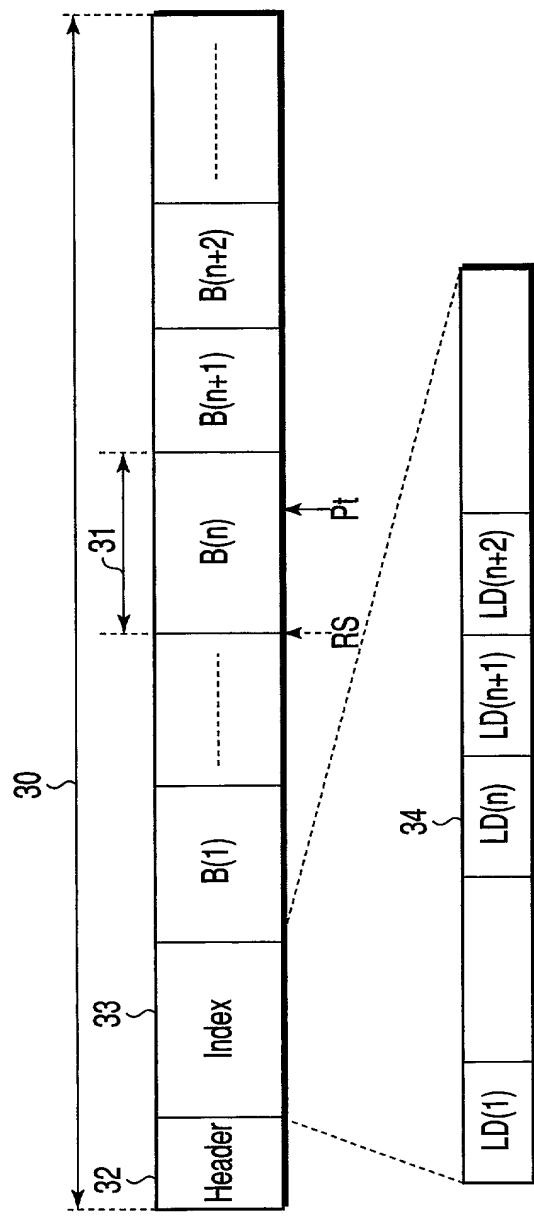
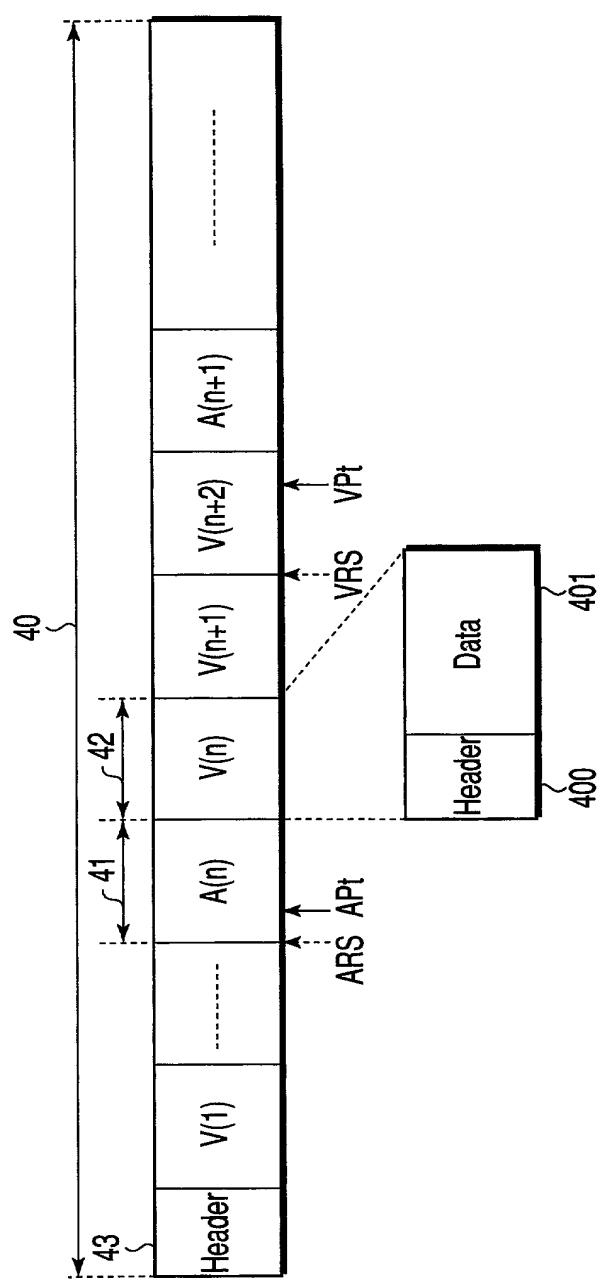


FIG. 2



F | G. 3



F | G. 4

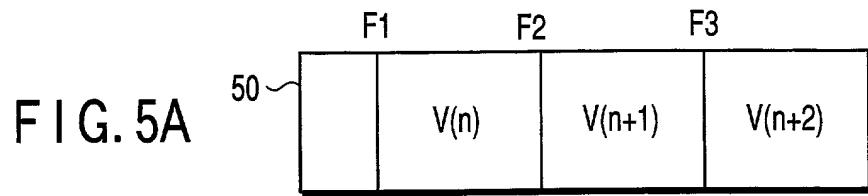


FIG. 5B

Block	Buffer address	Time
n	F1	Tb
n+1	F2	Tc
n+2	F3	Td
⋮	⋮	⋮

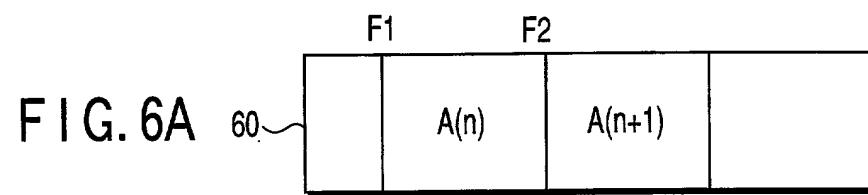
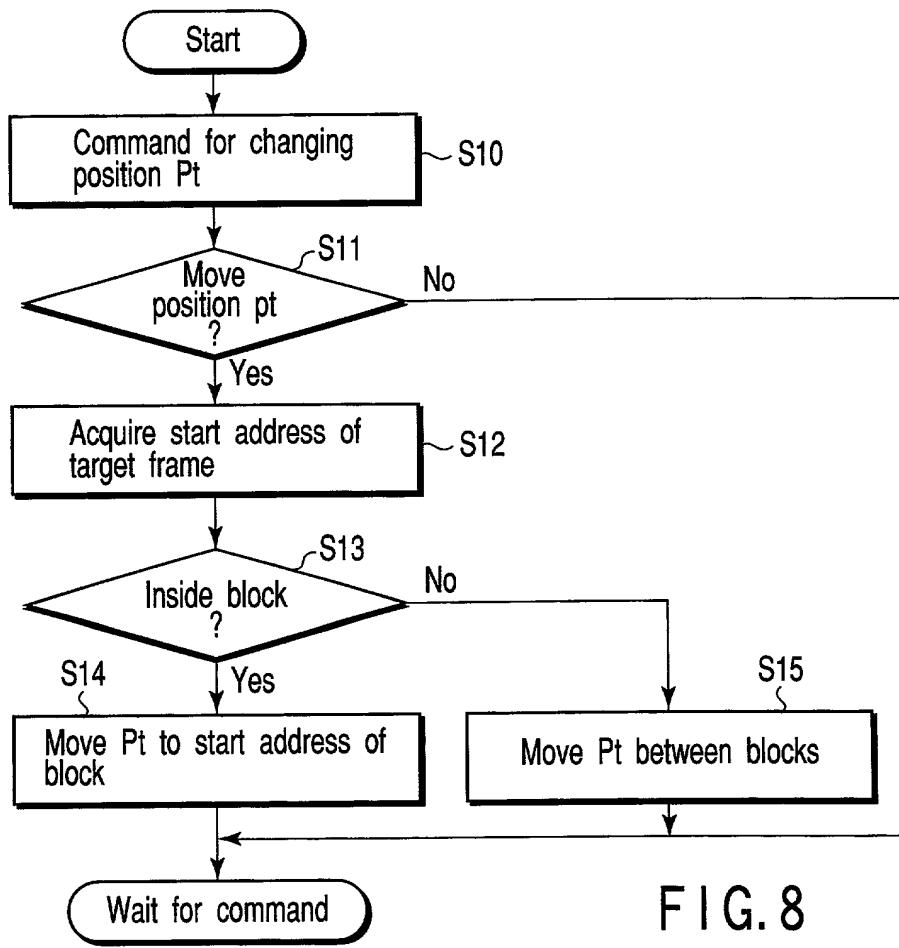
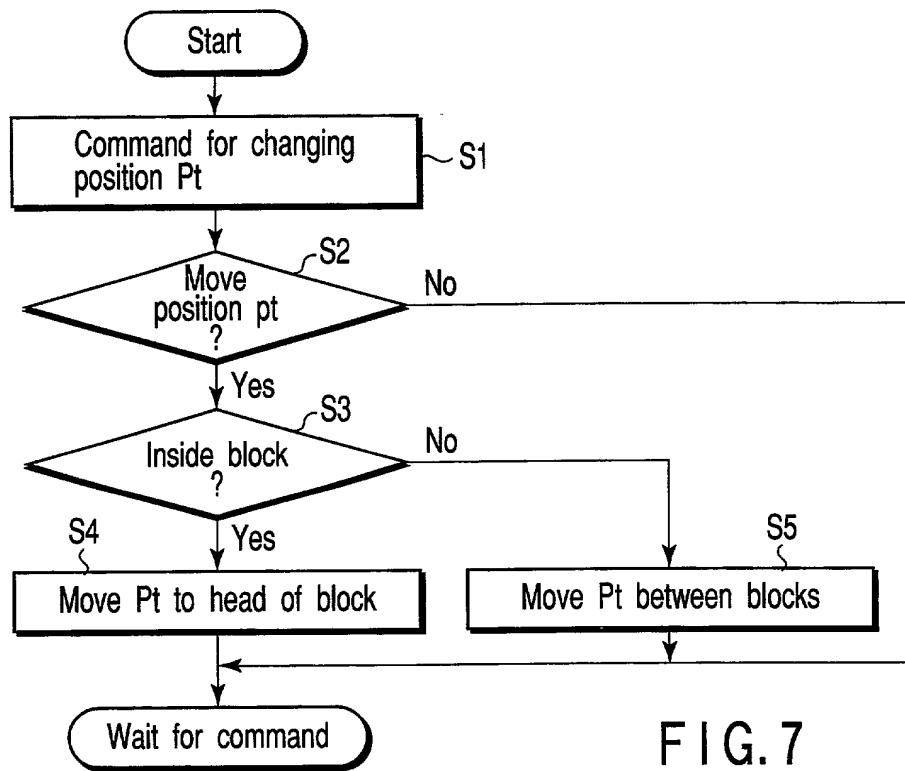


FIG. 6B

Block	Buffer address	Time
n	F1	Ta
n+1	F2	Te
⋮	⋮	⋮



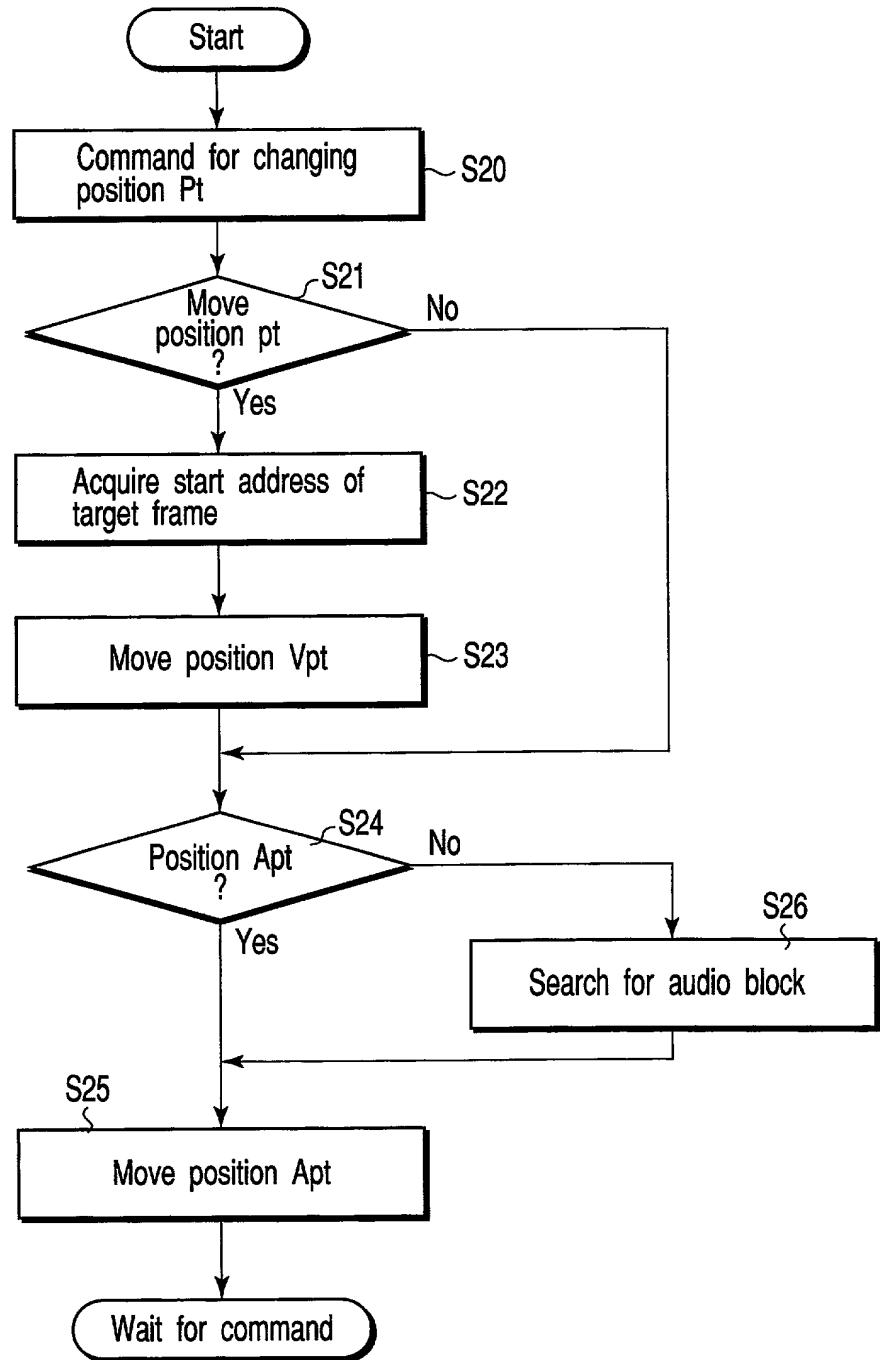


FIG. 9